## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: To Be Assigned Examiner: To Be Assigned

In Re PATENT APPLICATION Of:

Applicants	:	Kiyonori WATANABE	)
Serial No.	:	To Be Assigned	)
Filed	:	October 31, 2003	) INFORMATION ) DISCLOSURE
For	:	SEMICONDUCTOR DEVICE WITH IMPROVED DESIGN FREEDOM OF EXTERNAL TERMINAL	) STATEMENT ) )
Attorney Ref.	:	OHG 144	) October 31, 2003

Commissioner for Patents P. O. Box 1450 Alexandria VA 22313-1450

Sir:

This is an information disclosure statement submitted in compliance with the timing requirements of 37 C.F.R. §1.97(b)(1).

Attached is a copy of one Japanese publication. Any relevance of the Japanese publication can be gleaned from the attached English-language Abstract. The publication is listed on the attached Form PTO-1449.

Since this Information Disclosure Statement is being filed with the application, no certification or fee is required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to the document submitted. Consideration of the submitted document is respectfully requested.

Respectfully submitted,

October 31, 2003

Date

Robert H. Berdo, Jr. (Reg. No. 38,075)

RABIN & BERDO, P.C. CUSTOMER NO. 23995

(202) 371-8976

RHB:tz

(202) 408-0924 fax

FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT				Atty Docket	Applic	Application No.  To Be Assigned		
				OHG 144	To Be			
				Applicant				
				Yoshinori SHIZUNO				
				Filing Date	Group	Group Unit  To Be Assigned		
				October 31, 2003	To Be			
			U.S.	PATENT DOCUMENTS				
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